



Att'y Dckt Number: 2102397-911600

- 1 -

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Geeng-Chuan Chern

Application No. 09/916,618

Filed: July 26, 2001

For: SEMICONDUCTOR MEMORY ARRAY  
OF FLOATING GATE MEMORY CELLS  
WITH VERTICAL CONTROL GATE  
SIDEWALLS AND INSULATION  
SPACERS  
(as amended herein)

Group Art Unit: 2814

Examiner: Howard Weiss

**RESPONSE TO OFFICE ACTION  
MAILED JUNE 25, 2002**

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Box, Assistant Commissioner for Patents, Washington, DC 20231, on September 25, 2002.

GRAY CARY WARE & FREIDENRICH Date: 09/25/02

By: Kathleen LaBrie  
Kathleen LaBrie

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action mailed on June 25, 2002, please amend the above identified application as follows:

I. IN THE DRAWINGS

Please amend Figures 1A-1C by adding --(Prior Art)-- to the legends thereof, as indicated in red in the drawings submitted herewith.

II. IN THE TITLE

Please amend the title to read: --SEMICONDUCTOR MEMORY ARRAY OF  
FLOATING GATE MEMORY CELLS WITH VERTICAL CONTROL GATE SIDEWALLS  
AND INSULATION SPACERS--

C